EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	530	schedul$4 with ((graph subgraph subtree) and (common cluster shared "most frequently"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 15:46
S2	151	S1 and @rlad<"20030205"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 15:53
S3	8	S1 not S2 and ("control flow graph" CFG) and ((determin$4 classif$5 group$4) near8 (delay long path critical path edge))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 15:56
S4	0	S2 and ("control flow graph" CFG) and ((determin$4 classif$5 group$4) near8 (delay long path critical path edge))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:07
S5	0	S2 and ("control flow graph" CFG (intermediate near4 graph)) and ((determin$4 classif$5 group$4) near8 (delay long path critical path edge))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:07
S6	27	((((common shared) near2 processes) branch bound) near4 schedul$4) and ("control flow graph" CFG (intermediate near4 graph)) and ((determin$4 classif$5 group$4) near8 (delay long path critical path edge))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:09
S7	23	((((common shared) near2 processes) branch bound) near4 schedul$4) and S2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:09

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S8	79	(((((common shared) near2 processes) branch bound) near4 schedul$4) and S1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:33
S9	49	S8 not (S6 S7)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:33
S10	8	S9 and ("control flow graph" CFG (tree and (basic adj block)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/22 16:34
S11	11	(US-20030014743-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/23 11:58
S12	7	(loop and branch) and ((common share$2) near8 (subgraph subtree region) and ((CFG "control flow graph") same schedul$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 12:00
S13	0	(global near scheduling near region) and (branch same loop) and graph	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 12:44
S14	0	(global near scheduling near region) and (branch and loop) and graph	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 12:45

EAST Search History (Prior Art)

S15	0	(global near scheduling near region)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 12:45
S16	4	(global near scheduling near region$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 12:45
S17	2	(scheduling near4 region$2 near4 (shared common)) and CFG	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 13:35
S18	1	(scheduling same (subgraph near4 (shared common largest))) and (CFG "flow graph") and ("integrated circuit" FPGA ASIC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 13:46
S19	3	(scheduling near4 "ASAP") and (CFG "flow graph") and ("integrated circuit" FPGA ASIC) and (vector VHL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/23 14:02
S20	10	("20010016936" | "5491640" | "5502645" | "5572436" | "6226776" | "6324678" | "6353920" | "6564358" | "6604232" | "6795963").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/07/23 14:05
S21	5	S20 and schedul$4	US-PGPUB; USPAT; USOCR	OR	OFF	2010/07/23 14:06
S22	5	("5706205" | "5764951" | "6216252" | "6289491" | "6324678").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/07/23 14:09
S23	5	S22 and schedul$4	US-PGPUB; USPAT; USOCR	OR	OFF	2010/07/23 14:09

S24	22	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/24 15:12
S25	2	S24 and (DFG "control flow" with graph) and (cluster partition) and ((hardware near2 (design definition)) VHL RTL "integrated circuit") and ((route path branch delay) same schedul$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:15
S26	7	S24 and (DFG "control flow" CFG) and (cluster partition "basic block") and ((hardware near2 (design definition)) VHL RTL "integrated circuit" FPGA) and ((route path branch delay) same schedul$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:18
S27	5	S26 not S25	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:18
S28	4	S27 and ((shared common) with (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:25
S29	0	S28 and ((shared common) adj5 (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:28
S30	1	S24 and ((shared common) adj5 (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:28

EAST Search History (Prior Art)

S31	18	(schedul$4 same (VHDL VHL RTL)) and ((shared common) adj5 (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 15:53
S32	3	schedul$4 same ((largest and common) adj5 (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 16:12
S33	234	schedul$4 same ((common) adj5 (region block subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:21
S34	60	S33 and (ASAP FPGA VHDL RTL) schedul$4 same ((common) adj3 (region subgraph subtree))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:22
S35	1	S34 and MPEG4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:23
S36	8	S34 and ((circuit hardware) near5 (synthesis design))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:24

S37	25	(US-20030014743-$ or US-20010016936-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/24 18:28
S38	1	S37 and (schedul$4 same ((shared commmon recursive) near4 (task operation region subgraph instruction)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:29
S39	3	(VHDL RTL FPGA CFG) and (schedul$4 same (common near4 subexpression))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/24 18:45
S40	0	(DFD and CFG) and ((eliminat$4 replac$4 reduc$4 substitut$5) with (common adj3 (sub-expression subexpression)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:15
S41	34	(DFD CFG) and ((eliminat$4 replac$4 reduc$4 substitut$5) with (common adj3 (sub-expression subexpression)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:15
S42	2	S41 and (Verilog RTL VHDL VHL ASIC FPGA) and schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:16
S43	58	("6507947").URPN.	USPAT	OR	OFF	2010/07/25 14:18

S44	26	S43 and (Verilog RTL VHDL VHL ASIC FPGA) and schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:18
S45	1	S44 and (DFD CFG) and ((delay recursive) and (partition$4 cluster))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:20
S46	11	(CADDY ASAP) and "control flow graph"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:22
S47	8	S46 and (Verilog RTL VHDL VHL ASIC FPGA) and schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:23
S48	3	S47 and (DFD CFG) and ((delay recursive) and (partition$4 cluster))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:32
S49	28	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/25 14:52

S50	0	((commonly and shar$3) near4 subgraph) and ((schedul$4 partition$4) same ((data control) adj2 (flow and graph)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:54
S51	11	((commonly shar$3) near4 (group subgraph region subtree)) and ((schedul$4 partition$4) same ((data control) adj2 (flow and graph)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 14:54
S52	28	(US-20030014743-$ or US-20010016936-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/25 17:12
S53	2	S52 and Okada.in. and (shar$4 common)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 17:12
S54	28	(US-20030014743-$ or US-20010016936-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-7383529-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/25 18:55

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S55	0	S54 and subgraph and circuit and timing	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 18:55
S56	11	S54 and sub and circuit and timing	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 18:56
S57	8	S56 and (shar$4 common) near3 (CDFG region node tree operation descendant output resources)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 18:57
S58	11	S54 and CDFG and timing	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 19:04
S59	7	S58 and (merg$4 coalesc$5)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 19:05
S60	7	Gupta.in. and (subexpression near3 common)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 19:06
S61	1	S54 not Gupta and (subexpression near3 common)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/25 19:08

EAST Search History (Prior Art)

S62	27	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/26 09:06
S63	6	S62 and substrate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:06
S64	1	S62 not Dangelo and (placement same routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:10
S65	5	S62 not Dangelo and (placement routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:10
S66	7	S62 not S65 and (placement routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:11
S67	7	S62 not S65 and (routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:16

S68	0	S62 and (conductor same switch)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:47
S69	11	S62 and (conductor switch)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:47
S70	91	"716"/$.ccls. and (conductor same switch$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:50
S71	70	S70 and (placement routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:50
S72	1	S71 and (CFG CDFG DAG "control graph")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 09:50
S73	0	S62 and (CFG CDFG DAG "control graph") and FGPA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:00
S74	6	S62 and (CFG CDFG DAG "control graph") and FPGA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:00

S75	13	S62 and (CFG CDFG DAG "control graph") and array	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:00
S76	1	S62 and (merg$4 with schedul$5)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:20
S77	5	S62 and (basic adj block)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:21
S78	5	S77 and schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:24
S79	3	(combin$4 with schedul$4) and S62	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:25
S80	0	(combin$4 with schedul$4 with result) and CDFG	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:27
S81	20	(combin$4 with schedul$4 with result) and FPGA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:27

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S82	4	S81 and (placement routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:28
S83	2	((combin$4 merg$4 integrat$4) with schedul$4 with result) and (CDFG CFG DAG) and (netlist RTL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:29
S84	11809	(((two iterative successive) near2 (stage phase)) (first and second)) near6 schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:37
S85	2817	S84 and (placement routing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:37
S86	28	S85 and (CFG CDFG DAG "control graph") and (FPGA ASIC (circuit near design))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:38
S87	5	S86 and ((algorithm heuristic) same (schedul$4 partition$4 cluster$4 group$4 optimiz$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:39
S88	7	S62 and ((algorithm heuristic) same (schedul$4 partition$4 cluster$4 group$4 optimiz$4)) and (critical near path)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 10:40

S89	738	S84 and((consecutive iterative successive) near6 schedul$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:00
S90	12	S89 and (CFG CDFG DAG "control graph") and array	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:00
S91	23	(pcp (partial adj critical)) near5 schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:07
S92	1	S91 and (CFG CDFG DAG "control graph" "flow graph") and (FPGA ASIC (circuit near design) RTL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:08
S93	3	S91 and ((CFG CDFG DAG "control graph" "flow graph") (FPGA ASIC (circuit near design) RTL))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:08
S94	14	((branch near2 bound) and (partial critical path)) near6 schedul$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 11:52
S95	9	((basic adj block) same CDFG)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 15:29

S96	28	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$ or US-7120879-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/26 15:37
S97	6	S96 and (basic adj block)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 15:38
S98	14	S96 not S97 and (basic)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 15:52
S99	5	S96 and (longest and delay) with path	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/26 16:10
S100	28	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$ or US-7120879-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/27 11:50

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S10 1	20	S100 and delay	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 11:51
S10 2	6	S101 and (delay with (edge register))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 11:51
S10 3	0	S102 and branch near4 bound	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 11:52
S10 4	0	S100 and (branch near4 bound) and delay	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:02
S10 5	0	S100 and (branch near4 bound)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:02
S10 6	0	S100 and (branch-and-bound)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:02
S10 7	3	S100 and (allocation and threshold)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:39

S108	13	S100 and ((allocation time) with exceed$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:39
S109	1	S100 and subexpression	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 12:50
S110	11	S100 and "integrated circuit"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:20
S111	15	S100 and ASIC	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:20
S112	5	S111 not S110	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:20
S113	9	S100 and multiplexer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:39
S114	8	S113 and switch$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:39

S11 5	8	S114 and (integrated near circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:40
S11 6	0	S100 and sink	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 13:58
S11 7	11	S100 and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:01
S11 8	3	S100 and (reconfigur$5 with circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:02
S11 9	18	S100 and (circuit and schedul$3 and allocation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:13
S12 0	17	S119 and (DFG DAG (data near2 graph))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:14
S12 1	12	S120 and delay and (merg$4 select$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:14

S12 2	12	S120 and (data adj2 (flow graph)) and delay and (merg$4 select$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:58
S12 3	12	S122 and (data adj2 (flow graph)) and (CFG CDFG)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:58
S12 4	7	S123 and cluster$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 14:59
S12 5	6	S124 and (integrated near circuit$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 15:05
S12 6	0	S122 and (add$4 with edge)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 15:52
S12 7	1	S100 and (add$4 with edge)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 15:52
S12 8	0	S100 and (delay same (confirm$5 verif$4 ascertain$4 validat$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 16:38

S12 9	9	S100 and ((computation value path estimat$4 result) same (confirm$5 verif$4 ascertain$4 validat$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/27 16:39
S13 0	149	(((delay operation) and loop) same schedul$4) and (loop with (iteration and (count buffer)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:19
S13 1	12	S130 and ((integrated adj2 circuit) ASIC (FPGA and VHDL VLSI)) near4 (develop$5 build$4 design fabricat$4 manufactur$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:21
S13 2	5	S131 and (((number count) near3 iteration) same loop)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:32
S13 3	5	S132 and (buffer counter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:36
S13 4	1	S133 and (buffer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:36
S13 5	8	S131 and (buffer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:42

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S13 6	39	S130 and (loop near5 buffer$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:43
S13 7	3	S136 and ((integrated adj2 circuit) ASIC (FPGA and VHDL VLSI)) near4 (develop$5 build$4 design fabricat$4 manufactur$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:43
S13 8	1	S136 and (((integrated adj2 circuit) ASIC (FPGA and VHDL VLSI)) same (Netlist RTL schedul$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:45
S13 9	3	schedul$4 same "high-level synthesis" and (loop near5 buffer$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:46
S14 0	72	schedul$4 and (loop near5 buffer$3) and ((Netlist placement) and (routing RTL))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:47
S14 1	1	S140 and (schedul$4 same loop) and synthesis and (loop near5 buffer$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:48
S14 2	43	S140 not S141 and ((schedul$4 same loop) synthesis) and (loop near5 buffer$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:48

S143	3	S142 and (loop iteration) with ((input signal) near3 buffer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:52
S144	30	(US-20030014743-$ or US-20010016936-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$ or US-7120879-$).did. or (US-6745160-$ or US-7036106-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/28 10:55
S145	1	S144 and ((loop iteration pipeline) same (signal input read write) with buffer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:55
S146	2194	("flow graph" netlist placement) and ((loop iteration pipeline) same (signal input read write) with buffer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:56
S147	6	("flow graph" and netlist and placement) and ((loop iteration pipeline) same (signal input read write) with buffer) and S146	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:56

S148	31	(US-20030014743-$ or US-20010016936-$). did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$ or US-7120879-$).did. or (US-6745160-$ or US-7036106-$ or US-7401333-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/28 10:59
S149	14	S148 and buffer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 10:59
S150	6	S149 and (iteration number count) same loop	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:03
S151	0	S150 and Ahar.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:43
S152	2	S148 and (Ahar Ly).in. and (DFG CFG CDFG DAG)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:43
S153	3	S148 and (Ashar Ly).in. and (DFG CFG CDFG DAG)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:44

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S15 4	3	S153 and (loop near4 (count time iteration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:45
S15 5	3	S154 and (placement netlist layout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 11:45
S15 6	18	S148 and estimat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 12:24
S15 7	1	S148 and (estimat$3 near5 iteration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 12:25
S15 8	11	S148 and (estimat$3 with schedul$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 12:28
S15 9	0	S148 and ((lifetime (life near time) expectancy) near (element variant invariant variable)) same (iteration loop CGG)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:07
S16 0	0	S148 and ((lifetime (life near time) expectancy) near5 (element variant invariant variable)) same (iteration loop CGG schedul$4 unrolling)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:07

S16 1	0	S148 and ((lifetime (life near time) life expectancy) near5 (element variant invariant variable)) same (iteration loop CGG schedul$4 unrolling)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:07
S16 2	2	(loop near4 unrolling) and ((lifetime (life near time) life expectancy) near5 (element variant invariant variable)) same (iteration loop CGG schedul$4 unrolling)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:08
S16 3	5	(loop near4 unrolling) and ((lifetime (life near time) life expectancy (live near range)) near5 (element variant invariant variable)) same (iteration loop CGG schedul$4 unrolling)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:08
S16 4	32	(US-20030014743-$ or US-20010016936-$).did. or (US-6917909-$ or US-6314552-$ or US-6305006-$ or US-6260190-$ or US-6745222-$ or US-5557797-$ or US-6253372-$ or US-5317734-$ or US-5966534-$ or US-5943499-$ or US-6493863-$ or US-6324678-$ or US-5502645-$ or US-5572436-$ or US-6604232-$ or US-5706205-$ or US-6216252-$ or US-6289491-$ or US-5764951-$ or US-5555201-$ or US-5623418-$ or US-5801958-$ or US-6507947-$ or US-7155708-$ or US-RE40925-$ or US-7120879-$).did. or (US-6745160-$ or US-7036106-$ or US-7401333-$ or US-7000213-$).did.	US-PGPUB; USPAT	OR	OFF	2010/07/28 13:41
S16 5	0	S164 and (Ashar Ruttenberg Hammes).in. and ((placement netlist layout) and integrated near circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:42
S16 6	0	S164 and (Ashar Ruttenberg Hammes).in. and ((placement netlist layout VHL) and (integrated near3 circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:42

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S16 7	1	S164 and (Ashar Ruttenberg Hammes).in. and ((placement netlist layout VHL) and (reconfigur$5 with circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:43
S16 8	0	S164 and (Ashar Ruttenberg Hammes).in. and (ASIC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 13:51
S16 9	0	S164 and (Ashar Ruttenberg Hammes).in. and (largest longest slowest) near3 (slack delay path)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 14:20
S17 0	13	S164 and schedul$4 and (largest longest slowest) near3 (slack delay path)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/07/28 14:20

EAST Search History (Interference)

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